

IN THE SPECIFICATION:

Please amend the specification as follows (referring to the Substitute Specification filed on 10/17/2006):

Please amend paragraphs [0021] and [0022] as follows:

[0021] Fig. 7 shows a functional diagram of a Wallace tree in the diagram of Fig. 6;

[[and]]

[0022] Fig. 8 shows a functional diagram of the shift register unit from the functional diagram of Fig. 3[.]; and

Please insert the following new paragraph after paragraph [0022] and before the section heading "DETAILED DESCRIPTION OF THE INVENTION":

[0022.1] Fig. 9 shows the hierarchical instruction set of the data processing circuit.

Please amend paragraph [0045] as follows:

[0045] The As shown in Fig. 9, the hierarchical instruction set 90 of the data processing circuit 1 has the following instruction levels:

- Compiler based C code and C++ code 91 (compiler generated)
- RISC/CISC level assembly code 92 (macro function set)
- Free pipeline assembly code 93 (native machine language: graphs of pipeline sequences)
- Vector processing level 94 (set of macro functions for vector operations and user defined vector operations)

- Specific use of control registers and special purpose graphics hardware 95.